

SSC8L410TN6

N-Channel Enhancement Mode MOSFET

Features

V _{DS}	V _{GS}	R _{DS(ON)} Typ.	ID
40V		5.9mΩ@10V	70 4
	±20V	8.9mΩ@4V5	70A

> Description

This device is N-Channel enhancement MOSFET. Uses SGT technology and design to provide excellent RDSON with low gate charge. This device is suitable for use in DC-DC conversion, power switch and charging circuit.

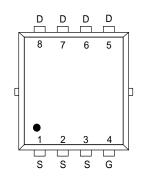
100% UIS + ΔVDS + Rg Tested!

- Applications
- Motor Drive Control
- DCDC Conversion
- Power Supplies
- Synchronous Rectification

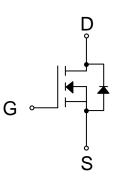
> Ordering Information

Device	Package	Shipping
SSC8L410TN6	PDFN5X6-8L	5000/Reel

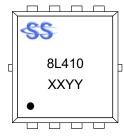
> Pin Configuration



PDFN5X6-8L (Top View)



Pin Configuration



<u>Marking</u>

(XXYY: Internal Traceability Code)





Symbol	Parameter	Ratings	Unit	
V _{DSS}	Drain-to-Source Voltage		40	V
V _{GSS}	Gate-to-Source Voltag	Gate-to-Source Voltage		V
	Continuous Duoin Current d	Tc =25 ℃	70	
ID	Continuous Drain Current d	Tc=100℃	38	A
	Continuous Drain Current ^a	T _A =25℃	20	
DSM		T _A =70℃	15	A
IDM	Pulsed Drain Current ^b		250	А
D		Tc =25 ℃	50	14/
PD	Power Dissipation ^c	Tc=100℃	20	W
Розм	Power Dissipation ^a	T _A =25℃	4.17	w
		T , =70 ℃	2.63	
las	Avalanche Current ^b L=0.5mH Single Pulse		15	А
Eas	Avalanche Energy ^b L=0.5mH Single Pulse		56.25	mJ
TJ	Operation junction temperature		-55~150	°C
Tstg	Storage temperature ra	-55~150	°C	

> Absolute Maximum Ratings ($T_A=25^{\circ}C$ unless otherwise noted)

➤ Thermal Resistance Ratings (T_A=25°C unless otherwise noted)

Symbol	Parameter	Ratings	Unit
Reja	Junction-to-Ambient Thermal Resistance ^a	30	°C/W
R _{θJC}	Junction-to-Case Thermal Resistance	2.5	C/ V

Note:

- a. The value of R_{θJA} is measured with the device mounted on 1 in² FR-4 board with 2oz.copper, in a still air environment with T_A=25 °C.The value in any given application depends on the user is specific board design. The power dissipation is based on the t≤10s thermal resistance rating.
- b. Repetitive rating, pulse width limited by junction temperature.
- c. The power dissipation P_D is based on T_{J(MAX)}=150°C, using junction-to-case thermal resistance, and is more useful in setting the upper dissipation limit for cases where additional heat sinking is used.
- d. The maximum current rating is package limited.



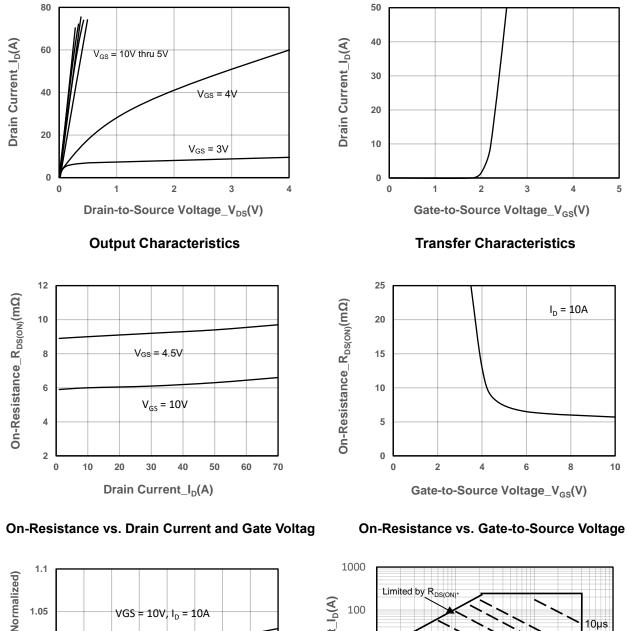


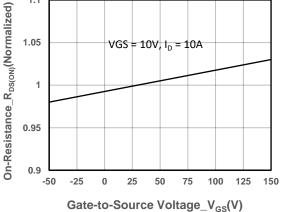
\succ Electrical Characteristics (T_A=25 °C unless otherwise noted)

Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit
Drain-Source Breakdown Voltage	V _{(BR)DSS}	V _{GS} = 0V, I _D = 250µA	40			V
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_D = 250 uA$	1	1.5	2.5	V
Drain Source On Registeres	Description	V _{GS} = 10V, I _D = 12A		5.9	7.7	mΩ
Drain-Source On-Resistance	$R_{DS(on)}$	V _{GS} = 4.5V, I _D = 10A		8.9	12	11152
Zero Gate Voltage Drain Current	ldss	V _{DS} = 40V, V _{GS} = 0V			1	μA
Gate-Source Leak Current	lgss	V_{GS} = ±20V, V_{DS} = 0V			±100	nA
Forward Voltage	V_{SD}	$V_{GS} = 0V, I_{S} = 1A$		0.78	1.2	V
Gate Resistance	R _G	V _{DS} = 0V, f = 1MHz		3.7		Ω
Input Capacitance	Ciss	$V_{DS} = 20V, V_{GS} = 0V,$		650		
Output Capacitance	Coss	f = 1MHz		360		pF
Reverse Transfer Capacitance	C _{RSS}			18		
Total Gate Charge	Q _G			11.5		
Gate to Source Charge	Q _{GS}	$V_{GS} = 10V, V_{DS} = 20V,$ $I_{D} = 12A$		2.1		nC
Gate to Drain Charge	Q_{GD}			2.2		
Turn-on Delay Time	T _{D(ON)}			8		
Rise Time	Tr	V _{GS} = 10V, V _{DS} = 15V,		6		
Turn-off Delay Time	T _{D(OFF)}	I _D = 1A, R _G = 3.3Ω		34		ns
Fall Time	T _f			10		
Diode Recovery Time	Trr	I⊧=20A, di/dt=500A/us		25		ns
Diode Recovery Charge	Qrr	l⊧=20A, di/dt=500A/us		60		nC

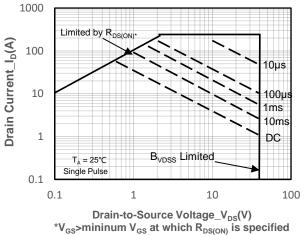


Typical Performance Characteristics (T_A=25℃ unless otherwise noted) \triangleright







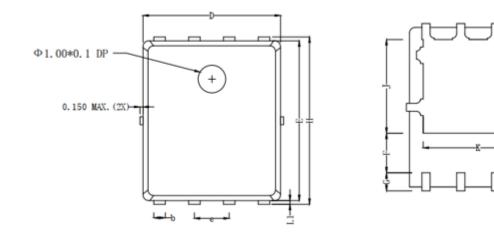


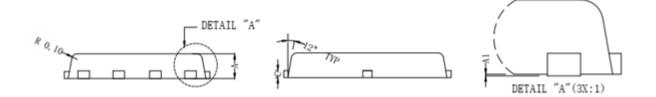
Safe Operating Area vs. Junction-to-Ambient





> Package Information





Symbol	Dimensions In Millimeters			
Symbol	Min.	Nom.	Max.	
A	0.90	1.00	1.10	
A1	0.00	0.03	0.05	
b	0.25	0.03	0.35	
С		0.254 REF		
D	4.80	4.90	5.00	
F	1.35 REF			
E	5.65	5.75	5.85	
е	1.27 BSC			
Н	5.90	6.00	6.10	
L1	0.10	0.13	0.16	
G	0.55 REF			
к	4.00 REF			
J	3.45 REF			



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